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INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use as many sheets as necessary)		Application Number	10/563,285-Conf. #4334
		Filing Date	January 3, 2006
		First Named Inventor	Yoshitoshi Kida
		Art Unit	2629
		Examiner Name	R. L. Willis
Sheet	1	of	1
		Attorney Docket Number	SON-3056

U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. ¹	Document Number Number-Kind Code ² (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear

FOREIGN PATENT DOCUMENTS						
Examiner Initials*	Cite No. ¹	Foreign Patent Document Country Code ³ -Number ⁴ -Kind Code ⁵ (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages Or Relevant Figures Appear	T ⁶
/R.W./	BA	EP-0 899 714 A2	03-03-1999	Sony Corp		
/R.W./	BB	EP-0 899 712 A2	03-03-1999	Sony Corp		
/R.W./	BC	WO-01/26088 A1	04-12-2001	Koninklijke Philips Electronics N.V.		
/R.W./	BD	WO-02/39420 A1	05-16-2002	Sony Corporation		✓
/R.W./	BE	EP-1 274 068 A2	01-08-2003	NEC Corporation		✓
/R.W./	BF	EP-1 333 422 A1	08-06-2003	Sony Corporation		✓

Examiner Signature	/Randal Willis/	Date Considered	01/27/2009
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NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
/R.W./	CA	Supplemental European Search Report issued November 25, 2008 for corresponding European Application No. 04 74 7374	
/R.W./	CB	Daubert, S.J et al: "Operation and Analysis of Current Copier Circuits" IEE Proceedings G. Electronic Circuits & Systems, Institution of Electrical Engineers. Stevenage, GB, Volume 137, no. 2 Part G, April 1, 1990, pages 109-115, XP00102778; ISSN: 0622-0039	

Examiner Signature	/Randal Willis/	Date Considered	01/29/2009
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